1. Investigation of the logic behavior of various gates using HDL:
2. 7400 quadruple two-input NAND gates - <https://www.edaplayground.com/x/MUQ8>
3. 7402 quadruple two-input NOR gates - <https://www.edaplayground.com/x/CudU>
4. 7404 hex inverters - <https://www.edaplayground.com/x/FbXQ>
5. 7408 quadruple two-input AND gates - <https://www.edaplayground.com/x/j9F6>
6. 7432 quadruple two-input OR gates - <https://www.edaplayground.com/x/et6S>
7. 7486 quadruple two-input XOR gates - <https://www.edaplayground.com/x/6uk6>
8. Using a single 7400 IC, connect and implement a circuit using HDL that

produces:

1. An inverter = <https://www.edaplayground.com/x/b6dj>
2. A two-input AND = <https://www.edaplayground.com/x/Ea45>
3. A two-input OR = <https://www.edaplayground.com/x/M4Ph>
4. A two-input XOR = <https://www.edaplayground.com/x/6xDy>
5. Construct & record the output of circuit using HDL that implements the

boolean function:

F=A (B+C)

1. Construct the circuit using Logic gates & verify the truth table =

<https://www.edaplayground.com/x/XuAA>

1. Construct the circuit using NAND gates only & verify the truth table=

<https://www.edaplayground.com/x/DxNv>